# The Programming Environment for Frontier

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### The Cray Programming Environment Mission



- Provide scalable performance, portability, and programmability on homogeneous and heterogeneous Cray systems
- Provide the best environment to develop, debug, analyze, and optimize applications for production supercomputing with tightly coupled compilers, libraries, and tools
- Address issues of scale and complexity of HPC systems
- Close interaction with users and quick turnaround on customer issues



### Cray's PE Vision for Accelerated Computing



- Most important hurdle for widespread adoption of accelerated computing in HPC is programming difficulty
  - Need a single programming model that is **portable across machine types** 
    - **Portable** expression of heterogeneity and multi-level parallelism
    - Programming model and optimization should not be significantly difference for "accelerated" nodes and multi-core x86 processors
    - Allow users to maintain a single code base
- Cray's approach to Accelerator Programming is to provide an ease of use tightly coupled high level
  programming environment with compilers, libraries, and tools that can hide the complexity of the system
- Ease of use is possible with
  - Compiler making it feasible for users to write applications in Fortran, C, and C++
  - Tools to help users port and optimize for hybrid systems
  - Auto-tuned scientific libraries

### **Building a Software Stack for Frontier**

- ORNL, LLNL, Cray, and AMD working together to deliver full software stack
  - CORAL-2 NRE
  - Provides Compiler and library choice
  - Includes:
    - Multiple programming environments
    - Performance and correctness tools
    - Optimizations such as:
      - Cray MPI GPU-to-GPU data movement
      - libsci\_acc
      - Cray PE DL Plugin
    - Compiler interoperability



## The Cray Compiling Environment (CCE)



- Cray technology designed for real scientific applications, not just for benchmarks
  - Arguably the most complete vectorization capabilities in the industry
    - Full automatic loop vectorization with automatic outer loop vectorization
      - no need for directives and source code modification
  - Automatic optimizations deliver performance for a new target through a simple recompile
  - Compiler optimization feedback for users with annotated listing of source code
  - Customized for our users
- Fully integrated heterogeneous optimization capability
- Fully Integrated and optimized PGAS
- Support multiple platforms
  - X86 (Intel or AMD)
  - ARM SVE+HBM (when available)
    - NSP-1 and ThunderX
  - AMD GPUs



### State of Cray's Existing C++ Compiler



#### • C++ Trends

- C++ usage in HPC has accelerated
- C++ language changes have accelerated too
  - Standards: 1998, 2003, 2011, 2014, 2017, 2020, ...
  - C++20 is expected to be a major change like C++11
- Clang leads C++ compilers in standards support and diagnostics
- Customer's view
  - Compile times are slow, especially when templates are used
  - C++11 was provided late (2015), whereas other compilers had full C++11 support earlier (2013) and experimental support before it was ratified
- Cray's view
  - Significantly more pressure to have a competitive C++ compiler!
    - Difficult to reduce compile time while maintaining same performance
    - Dependent on EDG schedule for supporting new standards
    - Lots of developer time spent maintaining non-differentiating passes common to all compilers

### Cray Compiling Environment – CCE 9.0





### CCE-Clang: What Will Be the Impact on Users?

- Cray will track Clang closely
  - Weekly or biweekly merges
- CCE major releases will follow Clang releases (about 2 months lag)
  - CCE will have more frequent major releases
  - Will follow the same version number (almost coincidentally)
- Cray-Clang is not Clang
  - Cray will support Cray-Clang
- What steps, if any, are we taking to open source programming environment tools like compilers, debuggers and profilers for existing and future accelerators?

## **Scalable Debugging on Cray Systems**



- Systems with thousands of threads of execution need a new debugging paradigm
  - Cray's focus is to build tools around traditional debuggers with innovative techniques for productivity and scalability
  - Scalable Solutions based on MRNet from University of Wisconsin (
    - s Tool 🚟 🖳 💽
- STAT Stack Trace Analysis Tool
  - Scalable generation of a single merged stack backtrace for the application
  - GUI based tool (stat-gui/stat-view) along with cli tools (stat-cl)
  - Gain insight into application behavior at a function level



#### ATP - Abnormal Termination Processing



- Scalable core file generation and analysis when application crashes
- Generates a merged stack backtrace akin to stat
- Selection algorithm to dump unique core files

#### • gdb4hpc



- Conventional CLI based interactive parallel debugger
- Look and feel of gdb syntax is inspired by gdb!
- Debug your application at scale
- CCDB Comparative debugging



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- A data-centric paradigm
- Compare two applications side-by-side
  - Focus on the data not state and internal operations
- GUI tool that interacts with gdb4hpc

#### • Valgring4hpc



- Parallel valgrind based debugging tool (memcheck)
- Aids in detection of memory leaks and errors in parallel applications
- Aggregates like errors across PEs/threads

### **Cray Performance Analysis Tools on GPUs**





Cray Performance Tools profiled production applications with over 256,000 ranks

- Provide whole program performance analysis
  - Single tool for CPU and GPU performance analysis
    - Single report can include statistics for both the host and the accelerator
    - Performance statistics includes accelerator time, host time, and amount of data copied to/from the accelerator
- Performance statistics mapped back to the user source by line number
  - Performance statistics grouped by accelerator directive
- Scaling (running big jobs with a large number of GPUs)
  - Results summarized and consolidated in one place

### Simplifying the Parallelization Task with Reveal

X Reveal OpenMP Scopin



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- **Reduce effort** associated with adding OpenMP to MPI programs
- Identify parallelization and scoping issues
- Get feedback on issues down the call chain (shared reductions, etc.)
- Get insight into optimizations performed by the Cray compiler
- Track requests to memory and evaluate the bandwidth contribution of objects within a program

### **How About OpenACC?**



• Are there any large applications in production with OpenMP for accelerators?

 How are we going to support existing OpenACC codebases, when we consider that an automatic tool will struggle to convert hundreds of thousands of lines of Fortran full of hand-written, multi-gen OpenACC directives to OpenMP?

 We are seeing that CUDA codes are more portable to AMD GPUs via HIP than OpenACC codes. What does this tell us about the purported portability benefits of directives?

### **Developer Environment for Frontier**





### Summary



- Cray/HPE will provide a high level programming environment for ORNL Frontier
  - Fortran, C, and C++ compilers
    - OpenMP Target directives
    - Compiler optimizations to take advantage of accelerator and multi-core X86 hardware appropriately
    - Support for new programming frameworks, such as RAJA and Kokkos
  - Cray Reveal
    - Scoping analysis tool to assist user in understanding their code and taking full advantage of SW and HW system
  - Cray Performance Measurement and Analysis toolkit
    - Single tool for GPU and CPU performance analysis with statistics for the whole application
  - Parallel debugger support with allinea DDT, ROGUE WAVE TotalView, or Cray gdb4hpc & CCDB
  - optimized on-node, GPU-GPU data movement operations and data movement operations between CPU- and GPU-attached memory regions using the ROCm interface directly
  - Auto-tuned Scientific Libraries support
    - Getting performance from the system ... no assembly required

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### QUESTIONS?



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